

LJTick-Divider



The LJTick-Divider (LJTD) is a signal-conditioning module designed to take 2 single-ended higher voltage analog signals, and divide them down to 0-2.5 volt signals. The 4-pin design plugs into the standard AIN/AIN/GND/VS screw terminal block found on newer LabJacks such as the U3, U6, UE9, and T7. The use of large resistors and a precision op-amp buffer provides an input impedance of 1 megaohm. By adding or replacing resistors, many other configurations are possible, as discussed in the datasheet.

The stock versions available from LabJack convert 0-10 volt (UNI10V) or +/-10 volt (BIP10V) inputs to 0-2.5 volts. If the custom range is selected (for an extra charge), specify the desired range in the order comments, and expect a 1-5 day delay. See the datasheet for typical custom options and the names used to specify.

For more information, see [the LJTick-Divider Datasheet](#).

RoHS:

Yes

Compatibility :

[T7](#)

[T7-Pro](#)

[U3-HV](#)

[U3-LV](#)

[U6](#)

[U6-Pro](#)

[UE9](#)

[UE9-Pro](#)